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Attorney's Docket No.: 10417-100001 / F51-136641M/TOM

2818

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Haruo Hyodo et al. Art Unit : 2818
Serial No. : 09/963,839 Examiner : Dung Anh Le
Filed : September 26, 2001
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEROF

5/ Elect
G. Stealy
827-2

Commissioner for Patents
Washington, D.C. 20231

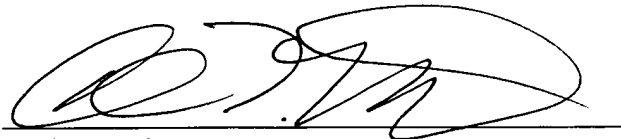
RESPONSE TO RESTRICTION REQUIREMENT

Responsive to the action mailed June 25, 2002, applicant elects the invention of Group I (claims 1 to 7) drawn to a semiconductor device. The election is made without traverse.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 7/22/02


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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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